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On July 3, 2006 By: Jan Huss

Appl. No. : 10/762,156 Confirmation No. 4963
Applicant : Naohito KOJIMA
Filed : January 20, 2004
TC/A.U. : 2811
Examiner : Steven Ho Yin LOKE

Docket No. : 2102487-991320 (352003)
Customer : 26379
No.

Title: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING AN OBLIQUE
GLOBAL SIGNAL WIRING AND SEMICONDUCTOR INTEGRATED
CIRCUIT WIRING METHOD

RESPONSE TO OFFICE ACTION

M/S: AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request the Examiner to consider the following Response in reply to the Office Action, mailed on March 2, 2006, for the above-referenced application. A Petition for an Extension of Time (one month) accompanies this communication.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.

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